

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	12	MCCallister-ronald-duane.in.	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 12:59	
2	BRS	L2	7	(digital adj2 subharmonic adj2 sampl\$3) and 1	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 13:02	
3	BRS	L3	5	(digital adj2 subharmonic) with (down adj2 conver\$6 or downconver\$6) same (predistort\$4 or pre adj2 distort\$4)	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 13:04	
4	BRS	L4	7	(digital adj2 subharmonic) with (down adj2 conver\$6 or downconver\$6) and (predistort\$4 or pre adj2 distort\$4)	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 13:04	
5	BRS	L5	2	4 not 3	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 13:04	
6	BRS	L6	0	5 not 1	US- PGPUB ; USPAT ; EPO; JPO	2006/04/26 13:04	

	Error Definition	Err ors
1		
2		
3		
4		
5		
6		

Day : Wednesday

Date: 4/26/2006

Time: 13:06:09

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MCCALLISTER

First Name = RONALD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09561222</u>	<u>6735734</u>	150	04/28/2000	MULTIPOINT TDM DATA DISTRIBUTION SYSTEM	MCCALLISTER, RONALD D.
<u>09635990</u>	<u>6366619</u>	150	08/09/2000	Constrained-envelope transmitter and method therefor	MCCALLISTER, RONALD D.
<u>09709885</u>	<u>6362701</u>	250	11/09/2000	Digital communications modulator having an interpolator upstream of a linearizer and method therefor	MCCALLISTER, RONALD D.
<u>10611793</u>	Not Issued	30	06/30/2003	Methods and apparatus for controlling signals	MCCALLISTER, RONALD D.
<u>10718505</u>	Not Issued	41	11/20/2003	Constrained-envelope transmitter and method therefor	MCCALLISTER, RONALD D.
<u>10718507</u>	Not Issued	41	11/19/2003	Constrained-envelope digital-communications transmission system and method therefor	MCCALLISTER, RONALD D.
<u>10732390</u>	Not Issued	30	12/09/2003	Methods and apparatus for controlling signals	MCCALLISTER, RONALD D.
<u>11176877</u>	Not Issued	41	07/06/2005	Constrained-envelope transmitter and method therefor	MCCALLISTER, RONALD D.
<u>60383750</u>	Not Issued	159	05/28/2002	Method of optimizing transmit power for E(DCF) based wireless networks and enhanced range WLAN	MCCALLISTER, RONALD D.
<u>06507193</u>	<u>4531224</u>	150	06/23/1983	LOW SIGNAL-TO-NOISE RATIO SYMBOL SYNCHRONIZER	MCCALLISTER, RONALD D.
<u>08306112</u>	<u>5440265</u>	150	09/14/1994	DIFFERENTIAL/COHERENT DIGITAL DEMODULATOR OPERATING AT MULTIPLE SYMBOL POINTS	MCCALLISTER, RONALD D.
<u>08327044</u>	Not Issued	161	10/21/1994	DIGITAL COMMUNICATION SYSTEM WITH TRANSPONDER THAT TRANSMITS SOFT DECISION	MCCALLISTER, RONALD D.

DATA					
<u>08468921</u>	5671257	150	06/06/1995	SYMBOL TIMING RECOVERY BASED ON COMPLEX SAMPLE MAGNITUDE	MCCALLISTER, RONALD D.
<u>08541337</u>	5949769	150	10/10/1995	MULTIRATE LOCAL MULTIPOINT DATA DISTRIBUTION SYSTEM	MCCALLISTER, RONALD D.
<u>08598893</u>	Not Issued	161	02/09/1996	LMDS SIGNAL REGENERATING METHOD AND NODE THEREFOR	MCCALLISTER, RONALD D.
<u>08620671</u>	5721756	150	03/26/1996	DIGITAL RECEIVER WITH TUNABLE ANALOG PARAMETERS AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>08627930</u>	5774084	150	04/03/1996	METHOD AND APPARATUS FOR TRANSLATING DIGITAL DATA INTO AN ANALOG SIGNAL	MCCALLISTER, RONALD D.
<u>08740016</u>	5818832	150	10/23/1996	RAPID SYNCHRONIZATION FOR COMMUNICATION SYSTEMS	MCCALLISTER, RONALD D.
<u>08800256</u>	5764102	150	02/13/1997	MULTI-STAGE SYMBOL SYNCHRONIZATION	MCCALLISTER, RONALD D.
<u>08912155</u>	5878085	150	08/15/1997	TRELLIS CODED MODULATION COMMUNICATIONS USING PILOT BITS TO RESOLVE PHASE AMBIGUITIES	MCCALLISTER, RONALD D.
<u>08912225</u>	5995551	150	08/15/1997	ROTATIONALLY INVARIANT PRAGMATIC TRELLIS CODED DIGITAL COMMUNICATION SYSTEM AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>08954762</u>	6078625	150	10/20/1997	PRAGMATIC DECODER AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>08991385</u>	6005897	150	12/16/1997	DATA COMMUNICATION SYSTEM AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>08994002</u>	6128282	150	12/18/1997	NETWORK NODE CONTROLLER AND METHOD FOR COMBINING CIRCUIT AND PACKET DATA	MCCALLISTER, RONALD D.
<u>09048612</u>	6097764	150	03/26/1998	PRAGMATIC TRELLIS-CODED MODULATION SYSTEM AND METHOD THEREFOR	MCCALLISTER, RONALD D.

<u>09095116</u>	6452948	150	06/10/1998	A METHOD FOR BAUD-CLOCK PHASE SYNCHRONIZATION IN A TDMA DIGITAL COMMUNICATIONS SYSTEM AND APPARATUS THEREFOR	MCCALLISTER, RONALD D.
<u>09095357</u>	6041088	150	06/10/1998	RAPID SYNCHRONIZATION FOR COMMUNICATION SYSTEMS	MCCALLISTER, RONALD D.
<u>09143230</u>	6104761	150	08/28/1998	CONSTRAINED-ENVELOPE DIGITAL-COMMUNICATIONS TRANSMISSION SYSTEM AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>09157457</u>	Not Issued	161	09/21/1998	APPARATUS FOR THE SELECTION OF A SIGNAL CHANNEL BANDWIDTH IN AN FDMA DIGITAL COMMUNICATIONS SYSTEM AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>09241697</u>	6337606	150	02/02/1999	DIGITAL COMMUNICATIONS MODULATOR HAVING A MODULATION PROCESSOR WHICH SUPPORTS HIGH DATA RATES	MCCALLISTER, RONALD D.
<u>09300624</u>	6392500	150	04/27/1999	ROTATIONALLY INVARIANT DIGITAL COMMUNICATIONS	MCCALLISTER, RONALD D.
<u>09303845</u>	6154510	150	05/03/1999	SYMBOL TIMING RECOVERY BASED ON ADJUSTED, PHASE-SELECTED MAGNITUDE VALUES	MCCALLISTER, RONALD D.
<u>09350485</u>	6141389	150	07/09/1999	DIGITAL TUNER	MCCALLISTER, RONALD D.
<u>09391055</u>	6507628	150	09/07/1999	DISTORTION-COMPENSATED DIGITAL COMMUNICATIONS RECEIVER AND METHOD THEREFOR	MCCALLISTER, RONALD D.
<u>09407132</u>	6222878	150	09/27/1999	COMMUNICATION SYSTEM WITH END-TO-END QUADRATURE BALANCE CONTROL	MCCALLISTER, RONALD D.
<u>09884000</u>	Not Issued	93	06/19/2001	REMOTE POWER AMPLIFIER LINEARIZATION	MCCALLISTER, RONALD DUANE
<u>10766768</u>	Not Issued	30	01/27/2004	Predistortion circuit and method for compensating linear distortion in a digital RF communications transmitter	MCCALLISTER, RONALD DUANE

<u>10766779</u>	Not Issued	20	01/27/2004	Predistortion circuit and method for compensating nonlinear distortion in a digital RF communications transmitter	MCCALLISTER, RONALD DUANE
<u>10766801</u>	Not Issued	71	01/27/2004	Distortion-managed digital RF communications transmitter and method therefor	MCCALLISTER, RONALD DUANE
<u>10840735</u>	Not Issued	30	05/06/2004	Predistortion circuit and method for compensating A/D and other distortion in a digital RF communications transmitter	MCCALLISTER, RONALD DUANE
<u>10871670</u>	Not Issued	30	06/17/2004	Equalized signal path with predictive subtraction signal and method therefor	MCCALLISTER, RONALD DUANE
<u>10971628</u>	Not Issued	30	10/22/2004	Equalized signal path with predictive subtraction signal and method therefor	MCCALLISTER, RONALD DUANE
<u>11012427</u>	Not Issued	30	12/14/2004	Transmitter predistortion circuit and method therefor	MCCALLISTER, RONALD DUANE
<u>11250853</u>	Not Issued	30	10/11/2005	Transceiver with isolation-filter compensation and method therefor	MCCALLISTER, RONALD DUANE
<u>11323686</u>	Not Issued	30	12/28/2005	Radio frequency transmitter and method therefor	MCCALLISTER, RONALD DUANE

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name

First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

Day : Wednesday

Date: 4/26/2006

Time: 13:06:03

 **PALM INTRANET****Inventor Information for 10/766801**

Inventor Name	City	State/Country
MCCALLISTER, RONALD DUANE	SCOTTSDALE	ARIZONA

[Appn Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity Data](#)[Foreign Data](#)Search Another: Application# or Patent# PCT / / or PG PUBS # Attorney Docket # Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page